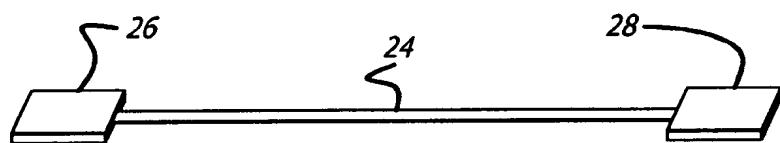
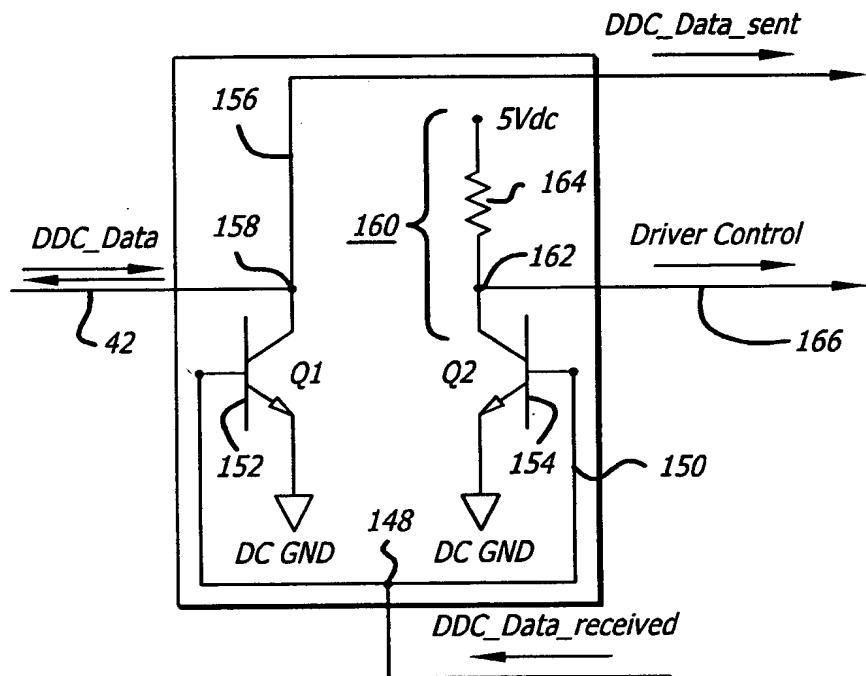


*FIG. 1(a)*  
(Prior Art)

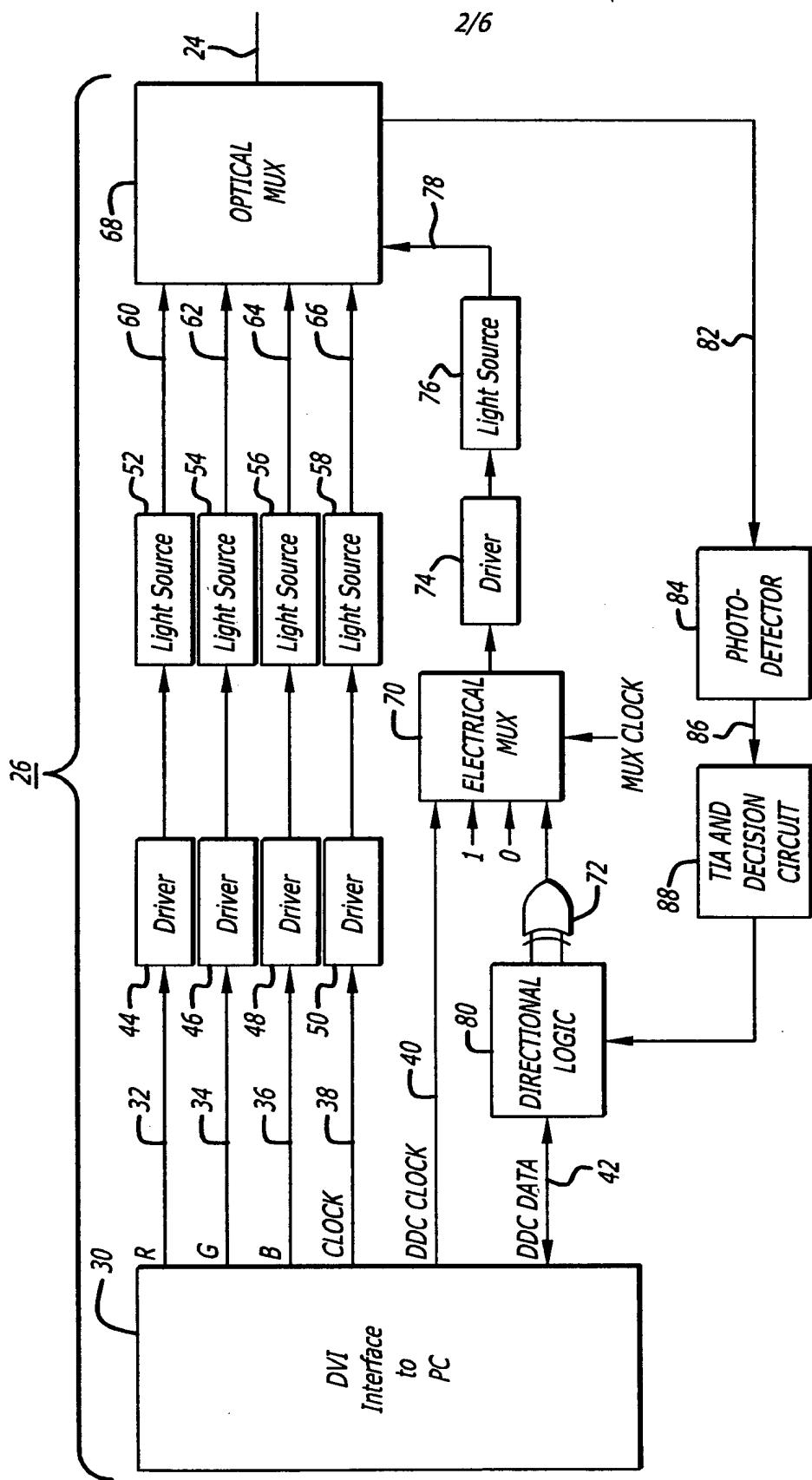


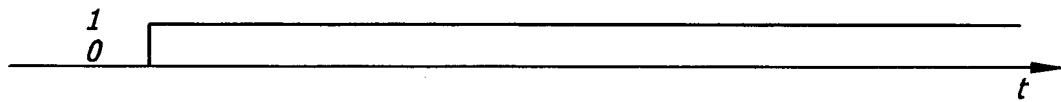
*FIG. 1(b)*



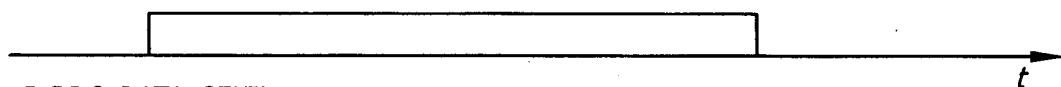
*FIG. 6*

FIG. 2



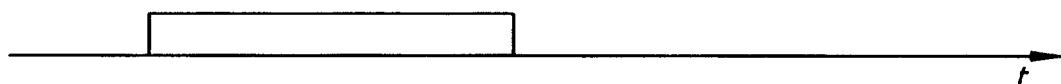


*FIG. 3(a)*



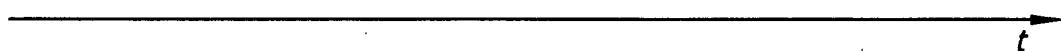
*B:DDC\_DATA\_SENT*

*FIG. 3(b)*

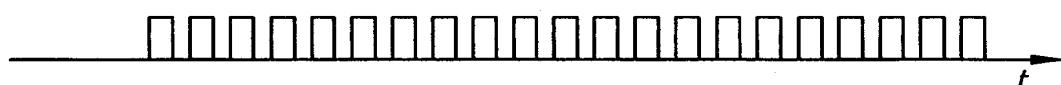


*A:DDC\_CLOCK*

*FIG. 3(c)*

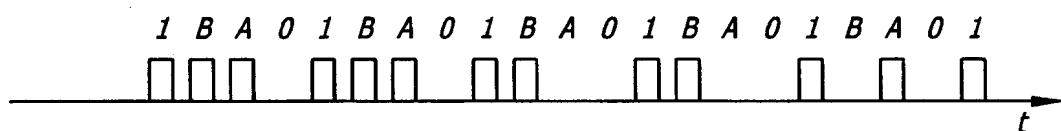


*FIG. 3(d)*



*MUX CLOCK*

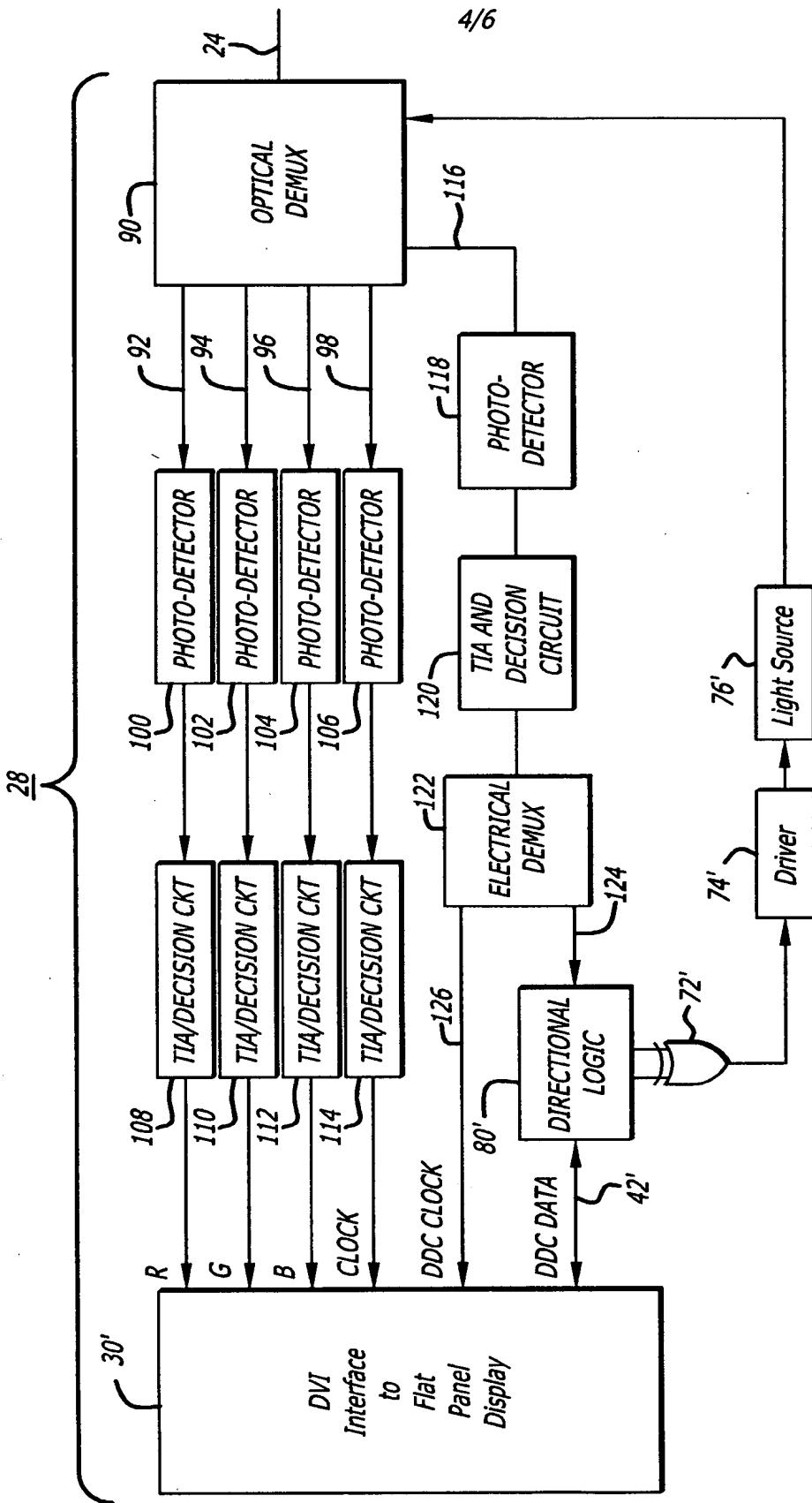
*FIG. 3(e)*



1 B A 0 1 B A 0 1 B A 0 1 B A 0 1 B A 0 1 B A 0 1

□ □

*FIG. (f)*



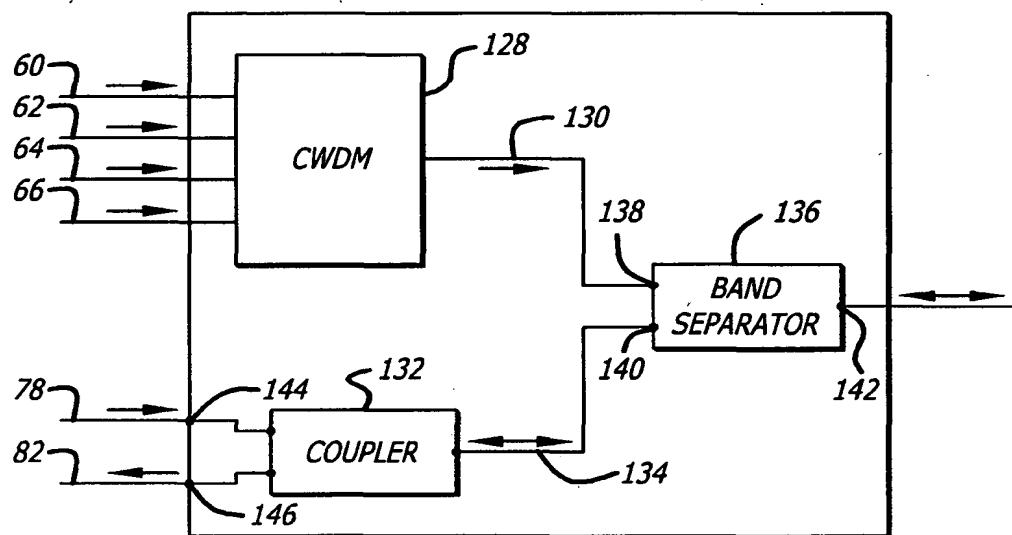


FIG. 5(a)

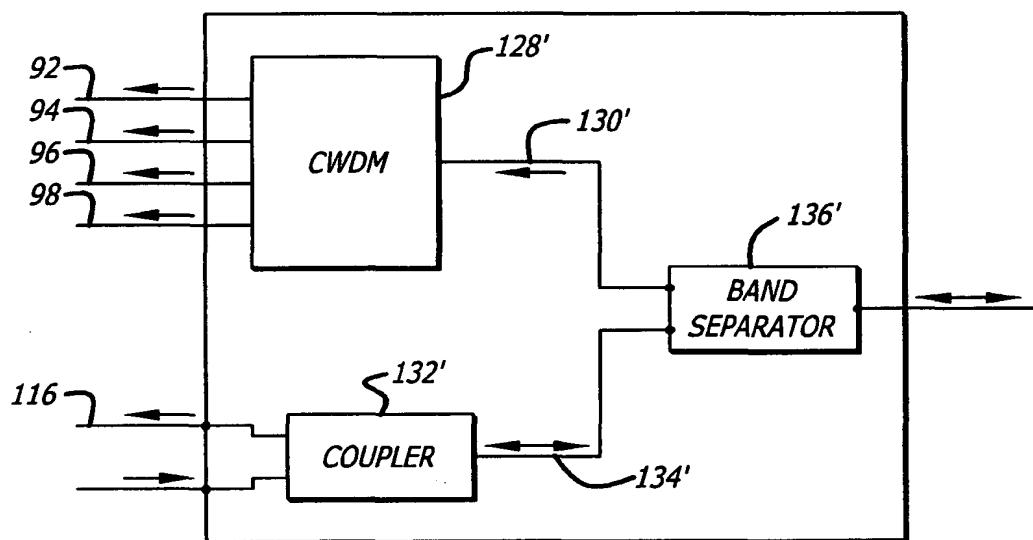


FIG. 5(b)

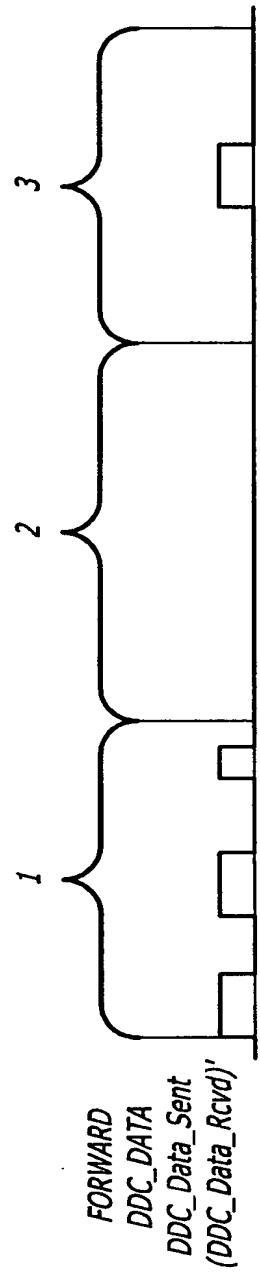


FIG. 7(a)

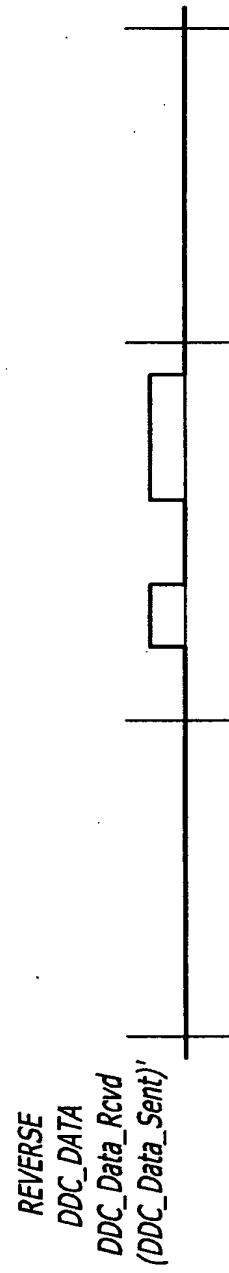


FIG. 7(b)